

Notice of References Cited

Application/Control No.

09/827,056

Applicant(s)/Patent Under
Reexamination
LIU ET AL.

Examiner

Thomas J. Mage

Art Unit

2811

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,355,527	03-2002	Yai-Fen Lin et al.	438/265
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf, "Silicon Processing for the VLSI Era, Vol. 2," Lattice Press, Sunset Beach, Ca., (1990), pp. 321-322.
	V	Tomohisa Mizuno, Shizuo Sawada, Yoshikazu Saitoh, and Takeshi Tanaka, "Hot-Carrier Injection Suppression Due to the Nitride-Oxide LDD Spacer Structure," IEEE Trans. on Electron Devices, Vol.38, No. 3, (1991), pp. 584-591.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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	K	US-			
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